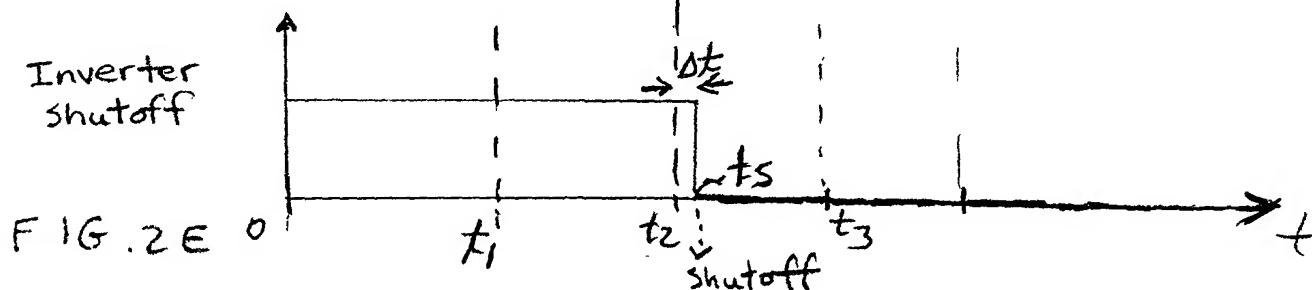
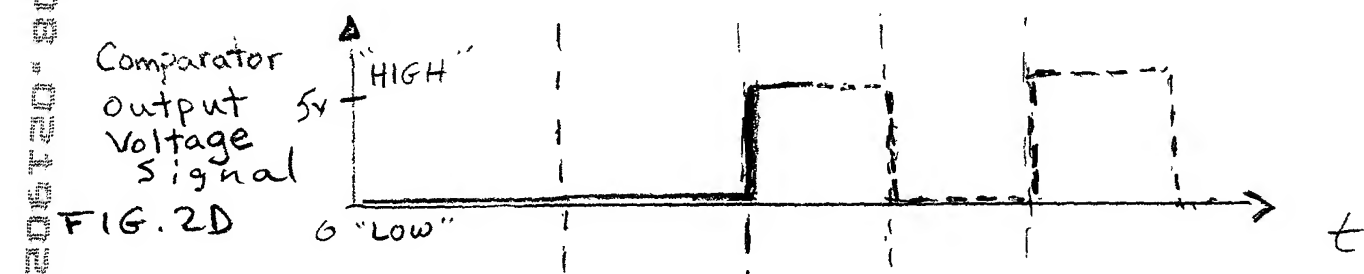
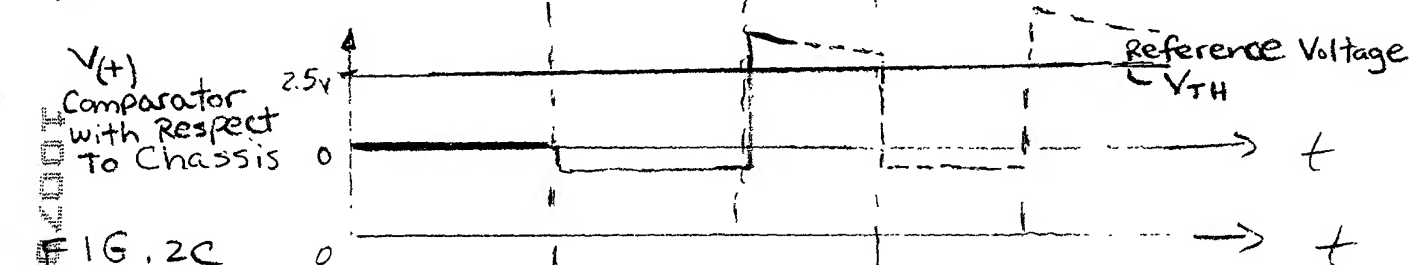
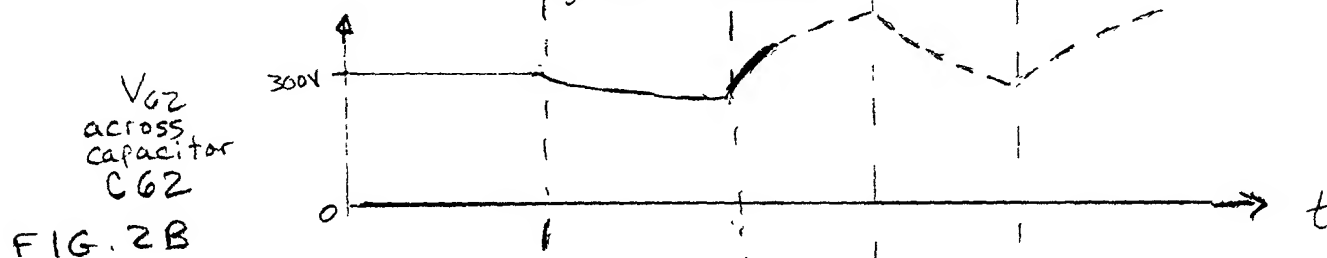
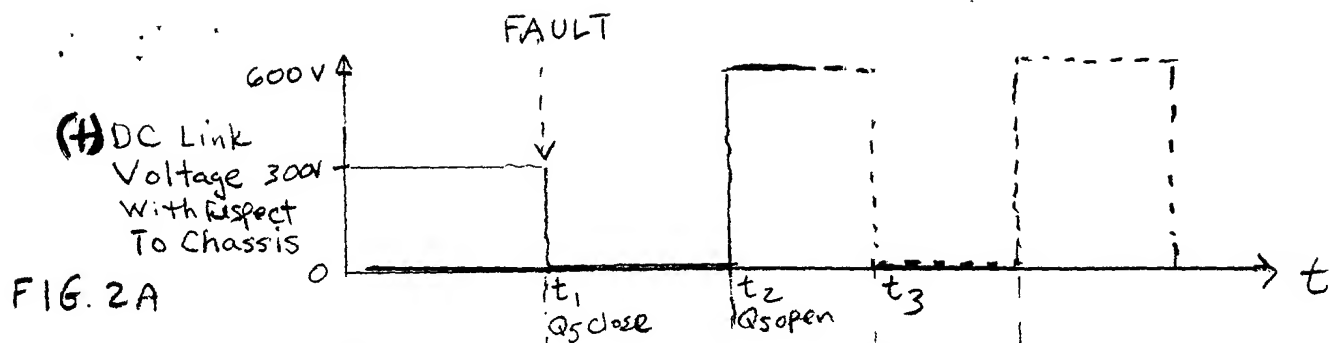
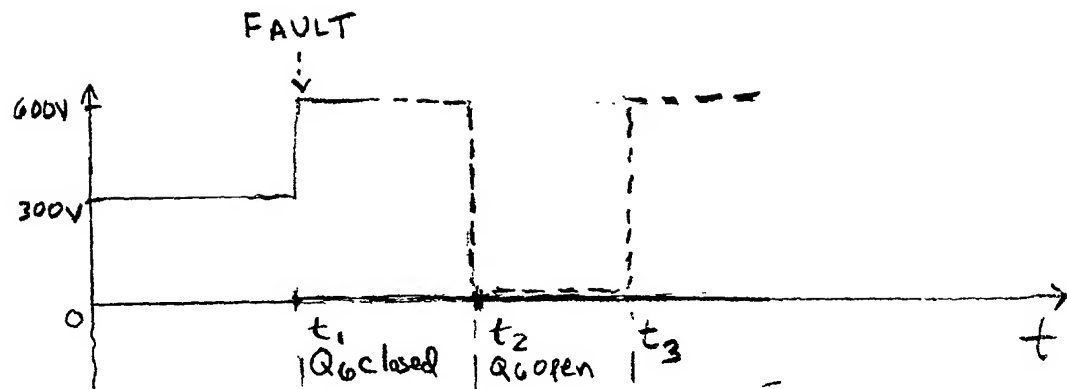


76-11



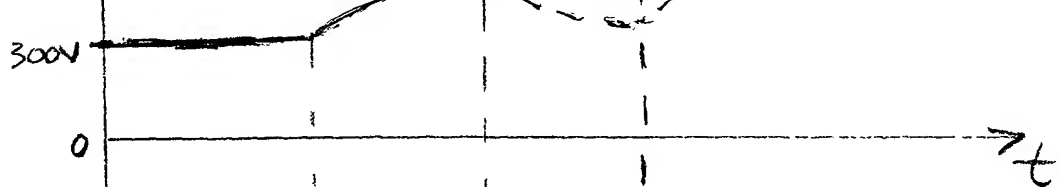
(+)DC Link Voltage
with Respect
to Chassis

FIG. 3A



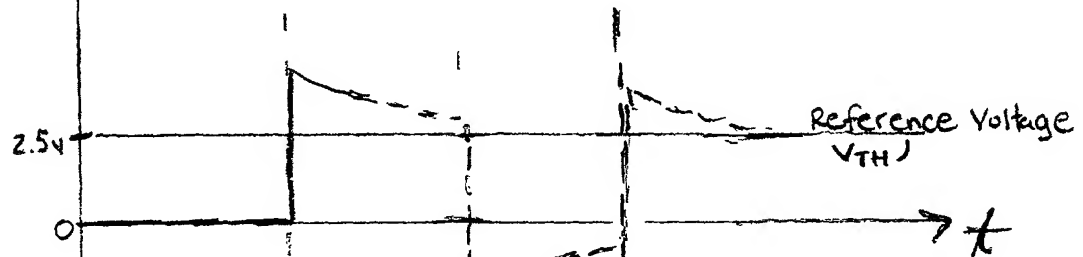
V_{C2}
across Capacitor
 C_2

FIG. 3B



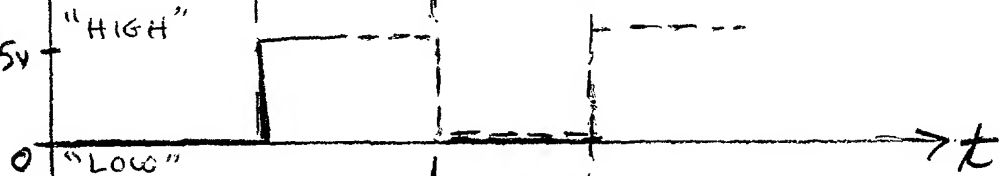
$V(t)$
Comparator
with Respect
to Chassis

FIG. 3C



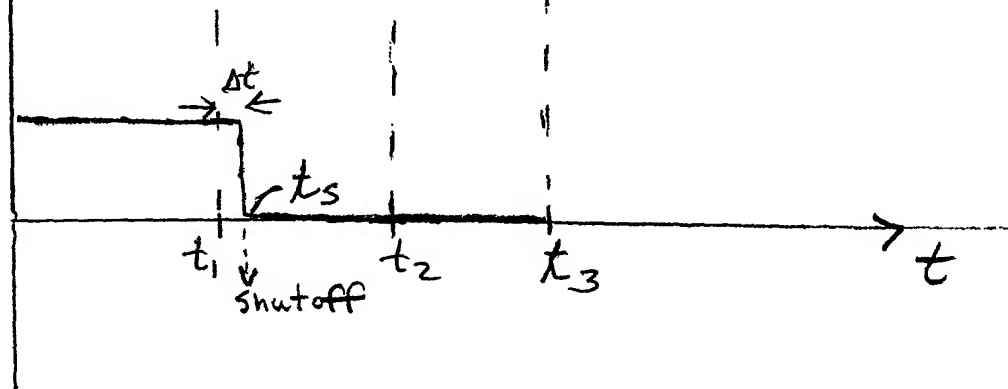
Comparator
Output Voltage S_v

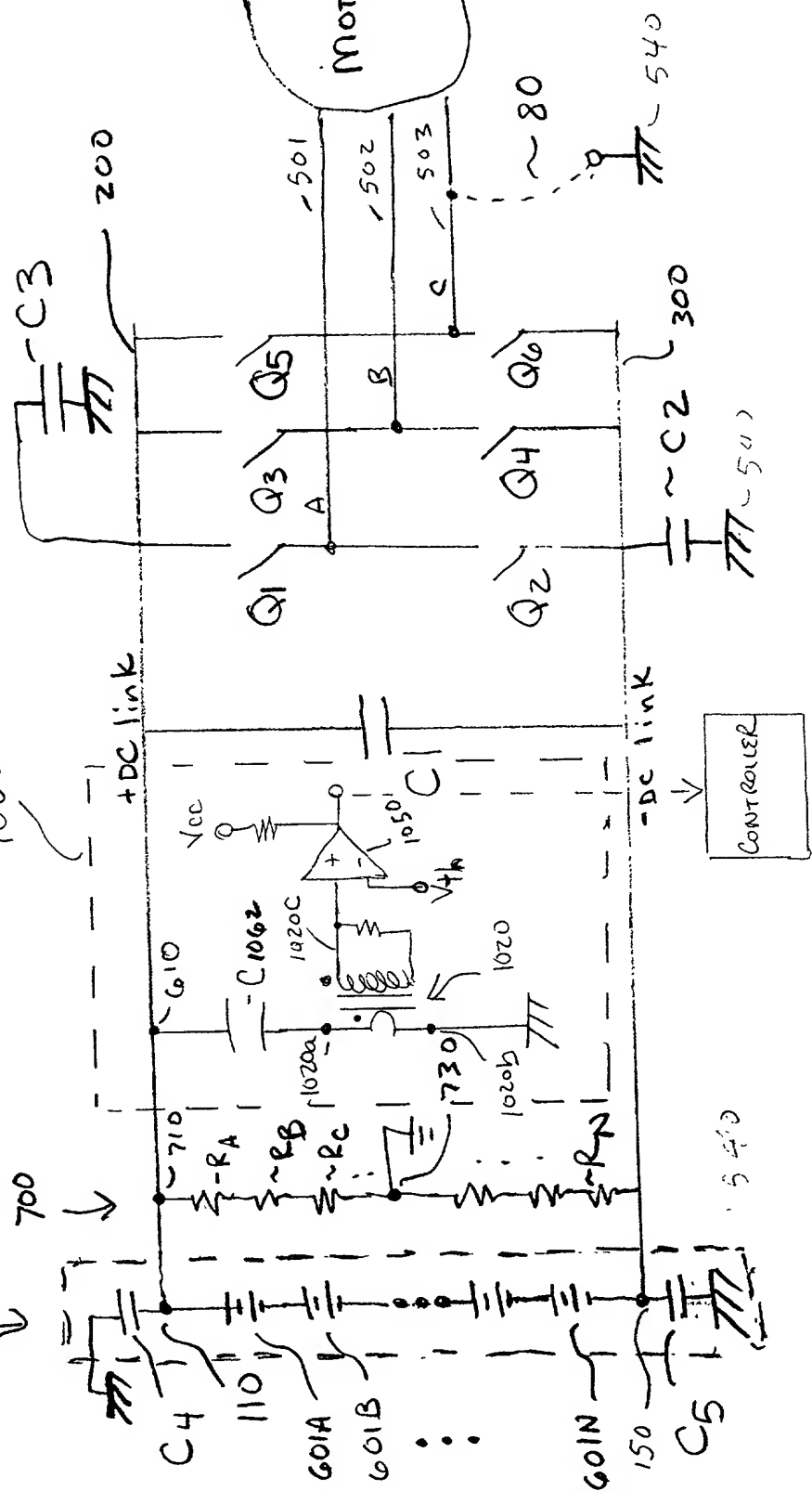
FIG. 3D



Inverter
Shutoff

FIG. 3E





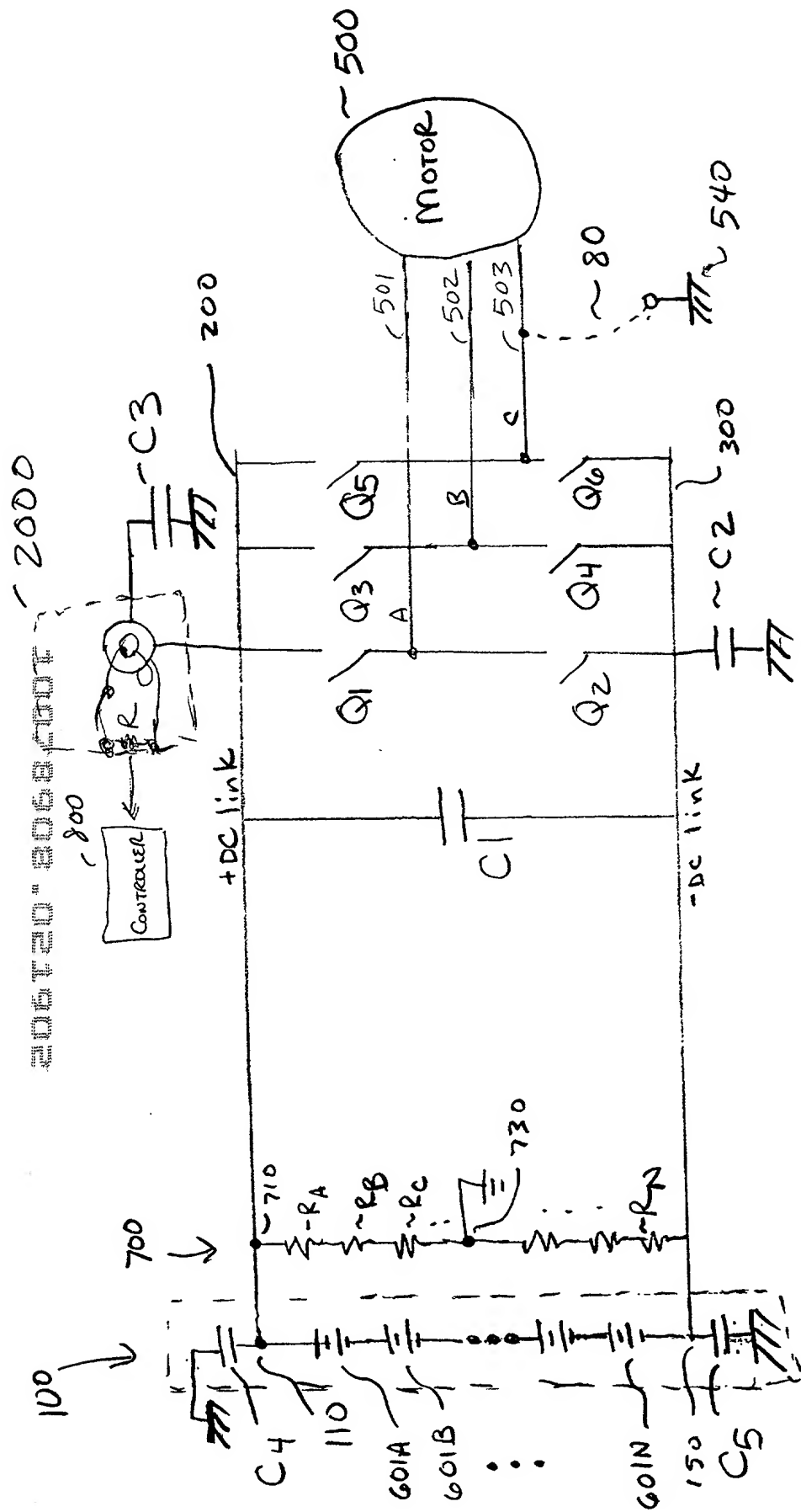


FIG. 5